Release Notes

Programming and Debug Tools v2025.2



Introduction

The Programming and Debug Tools stand-alone software is intended for laboratory and production environments where Libero[®] SoC Design Suite is not installed. The installer allows you to install the following tools:

- FlashPro Express programming software for Windows® and Linux®
- SmartDebug device debug tool for PolarFire[®] SoC, PolarFire, SmartFusion[®] 2, IGLOO[®] 2, and RTG4[™]
- Job Manager for Secured Production Programming Solution

Note: Libero SoC Design Suite includes FlashPro Express programming software, Job Manager, and SmartDebug by default.

Download Programming and Debug Tools v2025.2 Software

You can download the Programming and Debug Tools v2025.2 software from the Programming and Debug Tools page. You will need administrative privileges for installing or configuring the Programming and Debug Tools v2025.2 software on the Windows operating system.

System Requirements

This section provides information on supported operating systems, system memory requirements, and other recommendations.

Supported Operating Systems

Libero SoC Design Suite supports the following 64-bit operating systems:

- Microsoft® Windows 11.0
- Red Hat[®] Enterprise Linux (RHEL) 8.3-8.10, AlmaLinux[®] 8.3-8.10
- Ubuntu[®] 22.04.3 LTS



Attention:

- Libero SoC v2025.1 was the last release to support Windows 10.
- Siemens ModelSim Pro does not directly support the Ubuntu platform. However, users can successfully install and run ModelSim Pro and QuestaSim Pro ME on Ubuntu by installing the necessary libraries. Libero provides the script *check_linux_req.sh* to install required system packages for Ubuntu.
- Libero SoC design suite has been tested on x86 and x64 processor-based machines only.

System Memory Requirements

A minimum of 32 GB of Random-Access Memory (RAM) is recommended for implementing designs on MPF500T, MPFS460T, and RT PolarFire FPGA and SoC devices. For all other devices, a minimum of 16 GB of RAM is recommended.

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1. New in This Version

This section contains information about new features, new devices, and enhancements introduced in the Libero SoC Design Suite v2025.2.

1.1. Software Features and Enhancements

1.1.1. PolarFire, PolarFire SoC, RT PolarFire, and RT PolarFire SoC

1.1.1.1. FlashPro 6: SPI-Flash Programming Support for Winbond Flash Memory

Starting with Libero SoC v2025.2, FlashPro 6 has been enhanced with programming support for W25Q64J.

1.1.1.2. Expanded Live Probe Trigger Controls in SmartDebug

Libero SoC v2025.2 adds support for selecting the trigger signal's edge in SmartDebug when a signal assigned to Live Probe that acts as a trigger.

1.2. Resolved Issues

The following table lists the customer-reported defects and enhancement requests resolved in Libero SoC v2025.2 that have case numbers. Resolution of previously reported "Known Issues and Limitations" are also noted in this table.

Table 1-1. Customer-reported Defects and Enhancement Requests with Case Numbers

Case Number	Summary	Resolution
1556938	SmartDebug was displaying incorrect write-leveling results.	The write level tap delay value has been fixed by correctly reading the lower significant bits of the register.
01588834	The relationship between DQS position and delay	
01595754	tap width in MSS DDR differed from that in Fabric DDR.	left margins in the MSS DDR I/O margin. It has been resolved.
1543687	The CREATOR field inside the STP file displayed FlashPro improperly.	The CREATOR field inside the STP file has been updated to display Libero.
1585675	Incorrect message was displayed in the programming log of the G4 devices.	The incorrect message has been removed.



2. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description		
В	12/2025	The following change is made in this revision:		
		• Updated the supported operating system versions in the Supported Operating Systems section.		
		• Updated the "Case Number" column in Table 1-1 of the Resolved Issues section.		
Α	12/2025	Initial Revision		



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